

SM561-1 and SM562-1 ADSL Line Transformers



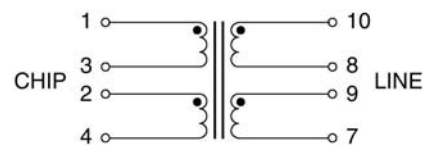
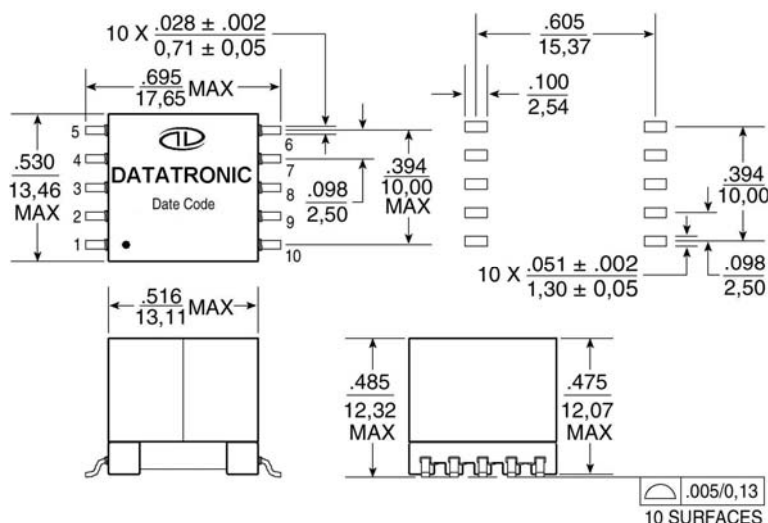
Features

- For use with Analog Devices AD20msp930 ADSL Chipsets
- Excellent Total Harmonic Distortion (THD) performance
- SMT Package and small size
- Designed to meet UL1950 and EN60950 Supplementary Insulation requirements for voltages up to 250Vrms maximum

ELECTRICAL SPECIFICATIONS AT 25°C: OPERATING TEMP: -40°C TO 85°C

	SM561-1	SM562-1
URNS RATIO (1-4) : (10-7)	1 : 1.1±2%	1 : 1.2±2%
OCL @10kHz, 0.1V		
(1-4) WITH (2-3) SHORTED	1.75mH MIN	1.75mH MIN
(10-7) WITH (8-9) SHORTED	2.03mH MIN	2.42mH MIN
LEAKAGE INDUCTANCE @100kHz 0.1V		
(1-4) WITH (2-3), (8-9), (10-7) SHORTED	7.5µH MAX	7.5µH MAX
LONGITUDINAL BALANCE 10kHz – 1.1MHz	50dB MIN	50dB MIN
INSERTION LOSS @300kHz	<0.5dB	<0.5dB
ISOLATION VOLTAGE (MAX)	1500 Vrms	1500 Vrms
LOAD RESISTANCE	100Ω	100Ω
DC RESISTANCE (1-4)	1.5Ω MAX	1.5Ω MAX
(10-7)	1.3Ω MAX	1.3Ω MAX
THD LINEARITY @30kHz	-80dB TYP	-80dB TYP
APPLICATION	CO	CPE

OUTLINE DIMENSIONS



SCHEMATIC

- NOTES:
1. Dimensions are shown in inches/millimeters
Unless otherwise specified, tolerance
= Inches: ±0.010
= Millimeters: ±0,25
 2. For non-RoHS parts replace DR prefix with 42-
(e.g. SM561-1 becomes 42-561-1)
 3. Terminal finish is compliant to RoHS requirements.
Solder in accordance with J-STD-020C.